



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/575,890	05/22/2000	Te-Hsiu Tsai	JCLA 5861	2752

7590 02/15/2002
J.C. PATENTS INC.
4 VENTURE
SUITE 250
IRVINE, CA 92618

EXAMINER

ABDULSELAM, ABBAS L

ART UNIT	PAPER NUMBER
----------	--------------

2674

DATE MAILED: 02/15/2002

3

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.
09/575,890

Applicant(s)
Te-Hsiu Tsai And Chiao-Yen Tai

Examiner
Abbas Abdulsalam

Group Art Unit
2674



☐ Responsive to communication(s) filed on _____

☐ This action is **FINAL**.

☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claims

☒ Claim(s) 1-27 is/are pending in the application.

Of the above, claim(s) _____ is/are withdrawn from consideration.

☐ Claim(s) _____ is/are allowed.

☒ Claim(s) 1-27 is/are rejected.

☐ Claim(s) _____ is/are objected to.

☐ Claims _____ are subject to restriction or election requirement.

Application Papers

☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on _____ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some* ☐ None of the CERTIFIED copies of the priority documents have been
☐ received.

☐ received in Application No. (Series Code/Serial Number) _____

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

☒ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____

☐ Interview Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

Art Unit: 2674

DETAILED ACTION

Claim Rejections 35 U.S.C. 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pariza et al.

(USPN 6268845).

Regarding claims 1, 13 and 25, Pariza teaches a monitor controller and USB controller for adjusting monitor attributes in any way desired and for communicating with the base system respectively. See column 3, lines 10-11. Pariza teaches a processor (100), A/D convertor (140), a ROM (134), and Memory/PCI/Cache controller (108) which provides a data path to a main memory (112). See column 4, lines 47-53 and Fig 1. Pariza teaches a USB monitor controller (302) along with a USB hub /peripheral controller (300) which provides a communication link and receives signals for passing to the base system. (B) via the USB (126). See column 9, lines 6-16 and see Fig 3-4. Pariza teaches selectors (316, 318) which provide data output at specific location as well as receive input from the switches (208, 218) and are connected to LED(210) and ground respectively. See column 9, lines 52-61, column 10,,lines 7-15 and Fig 4. Furthermore, Pariza teaches Port 3 (320) which supplies common signal (322) in order to turn the LED "ON" and "OFF". Pariza further teaches teaches that the controller is capable of writing and reading

Art Unit: 2674

data. See column 10, lines 16-25 and 25-33. However, Pariza does not specifically teach a recovery device for determining whether the data stored in the ROM are already updated according to the address and data signals so that switching of the starting device can take place. However, Pariza does teach Universal Host Control Driver, UHCD.SYS (420) which provides a software layer, interprets requests and builds data structures for the host controller (118). Pariza teaches the data structures in turn are built in a memory (112) containing all the necessary information to provide end to end communication. See column 12, lines 3-12.

Therefore, it would have been obvious to one having skill in the art at the time the invention was made to utilize the Universal Host Control Driver for the purpose of determining whether the data stored in the Rom are updated or not. One would have been motivated in view of Pariza that the Universal Host Control Driver is functionally equivalent to the desired recovery device.

Regarding claim 2, Pariza teaches the base system in connection to recording message and sending a message over the USB so as to announce the state of the message via a controller. See column 3, lines 58-63.

Regarding claims 3 and 14, Pariza teaches a computer system and USB (126) which allows computer peripherals to be configured. See column 5, lines 56-65.

Regarding claims 4 and 15, Pariza teaches USB monitor device driver (406) with a ring (3) interface. See column 11, lines 17-20.

Art Unit: 2674

Regarding claims 5 and 17, Pariza teaches bezel button controls which controls host computer system using LED to check the status. Pariza also teaches USB driver (414) which operates to pass commands to a certain location. See column 3, lines 1-2 and column 11, lines 38-40.

Regarding claims 6, 18 and 26-27, Pariza teaches a monitor controller for adjusting monitor attributes. See column 3, lines 10-13. Pariza teaches a monitor micro controller (302) providing monitor sleep signal (312) which controls LED (214). See Table 1 (column 7) and column 9, lines 39-45.

Regarding claims 7 and 19, Pariza teaches (USB) interface for base system connection and a digital converter (140) which receives composite video. See column 5, lines 5-7 and Fig 1.

Regarding claims 8, 16 and 20, Pariza teaches a processor (100), USB (122, 124), I/O controller coupled with ROM and memory (112). See Fig 1.

Regarding claims 9, 21 Pariza teaches an audio controller (152) which includes 16 bit stereo wave coder/decoder (CODEC), digital-analog converter (DAC) and a hardware master volume control. See column 5, lines 38-45.

Regarding claims 10 and 22, Pariza teaches a universal host control driver (420) along with a host controller (118) and a memory (112). See column 12, lines 3-12 and Fig 5.

Regarding claims 11-12 and 23-24, Pariza teaches the use of flash ROM and CD ROM (134). See column 3, lines 56-59 and Fig 1 respectively.

Art Unit: 2674

Conclusion

2. The prior art made of record and not relied upon is considered to applicant's disclosure.

The following arts are cited for further reference

U.S. Pat. No. 5,784,649 to Begur et al.

U.S. Pat No. 5,822,553 to Gifford et al.

U.S. Pat No. 6,138,240 to Tran et al.

U.S. Pat No. 6,289,466 to Bayramoglu et al.

Art Unit: 2674

3. Any inquiry concerning this communication or earlier communication from the examiner should be directed to **Abbas Abdulsalam** whose telephone number is **(703) 305-8591**. The examiner can normally be reached on Monday through Friday (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard Hjerpe**, can be reached at **(703) 305-4709**.

Any response to this action should be mailed to:

Commissioner of patents and Trademarks

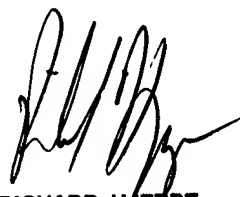
Washington, D.C. 20231

or faxed to:

(703) 872-9314

Hand delivered responses should be brought to crustal park II, Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology center 2600 customer Service office whose telephone number is (703) 306-0377.



RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600

Abbas Abdulsalam

Examiner

Art Unit 2674